

ABSTRACT OF THE DISCLOSURE

A gradation circuit is provided with an error diffusion circuit, a dither pattern circuit and a switch. The error
5 diffusion circuit converts a digital signal into a signal indicative of a level of gradation by an error diffusion method. The dither pattern circuit converts a digital signal into a signal indicative of a level of gradation by a dithering method. The switch selects for output between an output signal of the
10 error diffusion circuit and an output signal of the dither pattern circuit as on output signal of the gradation circuit.

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